



520.43863X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: H. OHTA et al
Serial No.: 10/849,211
Filed: May 20, 2004
For: SEMICONDUCTOR DEVICE AND MANUFACTURING
METHOD THEREFOR

Group: 2815
Examiner: E. LEE

AMENDMENT

Mail Stop: Amendment
Commissioner for Patents
POB 1450
Alexandria, VA 22313-1450

May 24, 2006

Sir:

In response to the outstanding non-final Office Action, dated January 24, 2006, the period of response for which extension of time is requested in the attached Petition for Extension of Time, please amend the above-identified application as listed below and as set forth on the following pages:

Amendments to the Title;

Amendments to the Specification;

Amendments to the Abstract;

Amendments to the Drawings;

Amendments to the Claims; and

05/25/2006 JADD01 00000013 10849211

02 FC:1201

400.00 DP

Remarks are included following the amendments.

An **Appendix** including a replacement sheet for drawing sheet 1/7 (Fig. 1) of the drawings is attached following the Remarks.

AMENDMENTS TO THE TITLE:

Please amend the title as listed below:

SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD
~~THEREFOR~~WITH A VERTICAL TRANSISTOR